Two C++ Tools*

Compiler Explorer and Cpp Insights

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*with a brief excursion into HW exploits

<u>Overview</u>

- Compiler Explorer and Cpp Insights look under the hood of C++ compilation.
 - Both kick off a compiler within the browser and show side-by-side source and output.
 - Both can be locally hosted.
- Compiler Explorer produces assembly output.
- Cpp Insights shows the output from the clang parser (specifically AST converted back to C++).

Compiler Explorer Basics

- Supports GCC and Clang plus many more.
- Multiarch including many ARM flavors.
- Arbitrary compiler options are supported.
- Settles a lot of arguments about what the compiler actually does.
- Has a wiki, FAQ.

<u>CE example: the "Spectre" exploit</u>

- Many security holes involving speculation execution by processors disclosed in recent years.
- Exploits now exist "in the wild."
- CE illustrates how the "retpoline" fix for C++ indirect branch speculation works.

C++ Indirect Branch

```
Example: A common C++ indirect branch
```

```
class Base {
 public:
 virtual void Foo() = 0;
};
class Derived : public Base {
 public:
 void Foo() override { ... }
};
Base* obj = new Derived;
obj->Foo();
```

The fix: "retpoline"

- *trampoline*: intermediary function that execution bounces off
- Takes advantage of the fact that in modern ISAs, "function return is itself an indirect branch. However, unlike other indirect branches, its target may be directly cached for exact future prediction at the point of function call."[source]
- retpoline strategy: make sure that a do-nothing branch keeps the processor busy so that the desirable branch has a chance to look up the correct address.

ASM without a retpoline

call	Derived::Derived() [complete object constructor]
mov	QWORD PTR [rbp-24], rbx
mov	rax, QWORD PTR [rbp-24]
mov	rax, QWORD PTR [rax]
mov	rdx, QWORD PTR [rax]
mov	rax, QWORD PTR [rbp-24]
mov	rdi, rax
call	rdx

With GCC and -mindirect-branch=thunk

Demo: -mindirect-branch=thunk

Clear or set this option to see the code with or w/o the retpoline.

<u>Diff with -mindirect-branch=thunk</u>

d <mark>iff -u</mark>	/home/al	ison/Peloton/Cpp-Tools/nothunk.asm /home/alison/Peloton/Cpp-Tools/thunk.asm
/hom	e/alison	<pre>/Peloton/Cpp-Tools/nothunk.asm 2020-01-09 05:27:00.915661235 -0800</pre>
+++ /hom	e/alison	<pre>/Peloton/Cpp-Tools/thunk.asm 2020-01-08 21:09:43.465911467 -0800</pre>
@@ -54,7	+54,7@	@
	mov	rdx, QWORD PTR [rax]
	mov	rax, QWORD PTR [rbp-24]
_		rdi, rax
-		rdx
+		x86_indirect_thunk_rdx
	mov	eax, 0
	add	rsp, 24
00 107	pop	rbx
@@ -107,		
	call	static_initialization_and_destruction_0(int, int)
	pop ret	rbp
+ v86 i		thunk rdx:
+	call	LIND1
+.LIND0:		
+	pause	
+	lfence	
+	imp	.LIND0
+.LIND1:		
+	mov	QWORD PTR [rsp], rdx
+	ret	

Cpp Insights Basics

- Clang only.
- Support for various C++ versions.

Demo with template and lambda instantiation

How does the preprocessor resolve auto?

```
7 template <typename T>
 8 void CalculateListStatistics(::std::list<T> *elemlist,
                                ::std::map<T, int> &countmap) {
 9
10
    for (T elem : *elemlist) {
11
       ::std::pair<T, int> candidate(elem, 1);
      auto result = countmap.insert(candidate);
12
13
      if (false == result.second) {
14
         result.first->second++:
15
       }
16 }
17 }
```

Maybe std::pair<T *, bool> result; ?



The result of template instantiation

The answer:

std::pair<std:: __map_iterator<std:: __tree_iterator<
std:: __value_type<long, int>,
std:: __tree_node<std:: __value_type<long, int>,
void *> *, long> >, bool> result

Freestanding Lambda Expressions are Classes

```
class lambda 19 16
public:
 inline long operator()() const
 { return (random() % static cast<long>((ELEMNUM - 1))); }
 using retType 19 16 = auto (*)() \rightarrow long;
 inline operator retType 19 16 () const noexcept
 { return invoke; };
private:
 static inline long invoke()
   return (random() % static cast<long>((ELEMNUM - 1))); }
};
```

```
_lambda_19_16 GetRand = __lambda_19_16(__lambda_19_16{});
```

Example: macros vs. constexpr

Demo: first CppInsights, then CompilerExplorer

Comparison: constexpr vs. C-style macro

• The input code:

#define CUBE(X) ((X) * (X) * (X))

constexpr Complex cubeme(const Complex &x) { return x * x * x; }

Calls sqrt() and cubeme() function each 1x.

<pre> constexprversion(): </pre>				
push	rbp			
mov	rbp, rsp			
sub	rsp, 32			
movsd	xmm0, QWORD PTR .LC2[rip]			
movsd	QWORD PTR [rbp-32], xmm0			
	xmm0, QWORD PTR .LC3[rip]			
movsd	QWORD PTR [rbp-24], xmm0			
lea	rax, [rbp-32]			
mov	rdi, rax			
call	<pre>sqrt(Complex_const&)</pre>			
	rax, xmm0			
	xmm0, xmm1			
	QWORD PTR [rbp-16], rax			
movsd	QWORD PTR [rbp-8], xmm0			
	rax, [rbp-16]			
	rdi, rax			
	<pre>cubeme(Complex const&)</pre>			
mov				
mov				
	rax, xmm0			
	rdx, xmm1			
nop				
leave				
ret				

constexpr code calls operator*() 2x, for a total of 1 sqrt() and 2 operator*() calls.

cubeme(Complex	const&):
push	rbp
mov	rbp, rsp
push	rbx
sub	rsp, 40
mov	QWORD PTR [rbp-40], rdi
mov	rdx, QWORD PTR [rbp-40]
mov	rax, QWORD PTR [rbp-40]
	rsi, rdx
mov	rdi, rax
call	
	rax, xmm0
movapd	•
mov	QWORD PTR [rbp-32], rax
movsd	
mov	rdx, QWORD PTR [rbp-40]
lea	rax, [rbp-32]
mov	rsi, rdx
	rdi, rax
call	<pre>operator*(Complex_const&, Complex_const&)</pre>
mov	eax, 0
mov	
	rax, xmm0
1	rdx, xmm1
	rcx, rax
	rbx, rdx
movq	xmm0, rcx
movq	
add	rsp, 40 rbx
рор	
pop ret	rbp
rec	

C-macro code calls sqrt() 3x and operator*() 2x.

stupidmacro():					
push	rbp				
mov	rbp, rsp				
sub	rsp, 80				
movsd	xmm0, QWORD PTR .LC2[rip]				
movsd	QWORD PTR [rbp-80], xmm0				
movsd	xmm0, QWORD PTR .LC3[rip]				
movsd	QWORD PTR [rbp-72], xmm0				
lea	rax, [rbp-80]				
mov	rdi, rax				
call	sqrt(Complex const&)				
movq	rax, xmm0				
movapd	xmm0, xmm1				
mov	QWORD PTR [rbp-64], rax				
movsd	QWORD PTR [rbp-56], xmm0				
lea	rax, [rbp-80]				
mov	rdi, rax				
call	sqrt(Complex const&)				
movq	rax, xmm0				
movapd	xmm0, xmm1				
mov	QWORD PTR [rbp-32], rax				
movsd	QWORD PTR [rbp-24], xmm0				
lea	rax, [rbp-80]				
mov	rdi, rax				
call	<pre>sqrt(Complex_const&)</pre>				
movq	rax, xmm0				
movapd	xmm0, xmm1				
mov	QWORD PTR [rbp-16], rax				
movsd	QWORD PTR [rbp-8], xmm0				
lea	rdx, [rbp-32]				
lea	rax, [rbp-16]				
mov	rsi, rdx				
mov	rdi, rax				
call	operator*(Complex_const&, Complex_const&)				
movq	rax, xmm0				
movapd	xmm0, xmm1				
mov	QWORD PTR [rbp-48], rax				
movsd	QWORD PTR [rbp-40], xmm0				
lea	rdx, [rbp-64]				
lea	rax, [rbp-48]				
mov	rsi, rdx				
mov	rdi, <mark>rax</mark>				
call	<pre>operator*(Complex_const&, Complex_const&)</pre>				

<u>Summary</u>

- Compiler Explorer and Cpp Insights make differences among compilers, compiler options and arches easier to understand.
- Pasting code into them is fast and painless.